

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	98447	(design\$4 or implement\$4)same message	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 11:50
L2	5969	(memory or storage)near9 thread and execut\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 11:50
L3	20718	integrated near5 circuit and fpga	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 11:52
L4	135298	memory same interfac\$4 same (connect\$4 or interconnect\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 11:53
L5	42	I1 and I2 and I3 and I4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 11:53
L6	27063	attribut\$4 near9(behavior\$3 or structur\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 11:54
L7	1	I5 and I6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/22 11:54